

EM-ZynqBox Application Ready FPGA Platform



Application Ready FPGA Platform with Camera Interfaces, Gigabit Ethernet, HDMI, Serial/CAN and Expansion Slot for Automotive (ADAS), Image Processing and Industrial Applications (IoT)

- Xilinx Zynq FPGA with integrated Processing System
- Reference Designs for advanced Image Processing
- Front Interfaces: Gigabit Ethernet, USB, HDMI, Serial/CAN and JTAG
- 5 LVDS Camera Interfaces (Rosenberger HSD)
- Expansion Slot for AMC modules
- Power Supply 12V DC
- Low Power (<20W)</p>
- Optional: fanless version

EM-ZynqBox

The EM-ZynqBox can be used for development e.g. of advanced image processing algorithms used in FPGA applications. Therefore it provides 5 camera interfaces with automotive HSD connectors.

It can be extended with optional AMC modules providing project specific hardware components and connectors.

Target Applications are:

- Automotive (ADAS)
- Advanced Image Processing
- Surveillance
- Industrial (IoT)

EM-ZynqBox is designed for development, test and integration and deployment in machines or vehicles.

Technical Information

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Туре	FPGA Box	Xilinx FPGA Zynq-7000 Device (Zynq Ultrascale+ in preparation)	
		SoC CPU included in FPGA package	
		Two banks of DDR3 (FPGA 64-bit, CPU 32-bit)	
Interfaces	Ethernet	2x Gigabit Ethernet RJ-45	
	USB	2x USB 2.0	
	Graphic Ports	1x HDMI	
	Serial Ports	2x RS232, optional: CAN	
	Camera Interfaces	5x LVDS (HSD)	
	JTAG	for Xilinx Platform Cable USB II	
Expansion (optional)	AMC	Single-width full-size AMC module	
Mechanical	Mounting	Desktop	
	Dimensions (W x H x D)	310 x 44 x 221 mm	
	Weight	1.5 kg	
Power	Input Voltage Range	12 VDC	
Power Consumption	Typical	<20 W	
Environment	Operating Temperature	-5°C to 45°C (optional: -20°C to +70°C)	
	Storage Temperature	-40°C to +85°C	
	Relative Humidity	95% @ 40°C (non-condensing)	
	Operating Vibration	5 to 500Hz, 1G	
	Operating Shock	30Gs, each axis	
Software Support	Linux	PetaLinux V2014.4 Board Support Package	
		Pre-Installed in QSPI with file system on SD card	
Reference Designs	Base I/O Channels	VHDL/Vivado	
		Default FPGA Image in Flash	

Ordering Information

Configuration	Description	Order Code
EM-ZynqBox 7045	ZynqBox, Standard, 12V with Xilinx Zynq 7045 FPGA	A09552
EM-ZynqBox 7100	ZynqBox, Standard, 12V with Xilinx Zynq 7100 FPGA	A09553

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